

**Amendments to the Specification:**

Please replace the paragraph bridging pages 17 and 18, with the following rewritten paragraph:

Q1 Fig. 7 is a block diagram showing an edge correction apparatus for a digital video camera according to the first embodiment of the present invention. The edge correction apparatus shown in Fig. 7 is different from the conventional apparatus shown in Fig. 1 in that an edge correction processing means 121 ~~comprises~~ contains a vertical edge component suppression position detector 141. In this digital video camera, as described in "DESCRIPTION OF THE PRIOR ART", an analog video signal output from an image sensing element 101 is converted into a digital signal by an A/D converter 102, subjected to OB clamping processing, and separated into red, blue, and green signals by a color separation processor 104. Since arithmetic processes given by equations (1) to (5) are required, color processing is done using an image signal having undergone OB clamping processing and image signals obtained by delaying this image signal by one horizontal line and two horizontal lines by 1H delay lines 115 and 116. This is a color separation method generally called 3-line processing. For green, green signals G1 and G2 are generated which are respectively delayed by one horizontal line and two horizontal lines so as to be used for vertical edge correction processing by a vertical edge signal generator 111. This generation method is the same as the conventional method.